

The image shows the ePAQ-9425 Multifunction Gateway circuit board. It is a blue PCB with various ports and components. The top right corner features the product name "ePAQ-9425" and "Multifunction Gateway". Below this, there are four sets of port headers labeled RS232C/RS485, each with two rows of pins and labels like TX, RX, DTRM, DTRF, LPTAK, LPTBK, LPTCK, LPTDK. In the center, there are sections for RS-422 and RS-423, each with two rows of pins and labels like LIA, LIFB, LITK, LITD, LIAA, LIAB, LITL, LITR. To the left of these are sections for IRIG/SERIAL, SD CARD, MAINT, and HOST, each with specific pin labels. On the far left, there's a section for 10/100 BASE TX and 100 BASE FX with pins for LIA, LIFB, LITK, LITD, LIAA, LIAB, LITL, LITR. At the bottom left, there's a modem section with pins for TX, RX, DTRM, DTRF, LPTAK, LPTBK, LPTCK, LPTDK. The board also has several jumpers and switches, including a PWR switch and a MODEM switch.

- ***Interface with all devices using any open Protocol and over any media***
- ***Front Panel Serial Fiber or Rear Panel DB-9 Serial Port Connections for ease of installation within existing racks***
- ***Redundant architecture for critical applications***
- ***Easy, Modular Expansion for additional Hardware I/O Points***
- ***Security Tools available for meeting latest NERC-CIP and Cyber Security Requirements***
- ***Quick and easy configuration with provided ConfigWiz® Windows Software***
- ***SD Memory Card and USB Ports for easy, but secure, User Expansion and Update***
- ***Embedded Web Server and HMI for Local or Remote Display and Configuration***

SPECIFICATIONS

Ports Capacities	3U rack space, front or rear mounted 2-Bell 202T telecom legacy I/O 2-RS-232C (bit or byte) 1-SD/SDHC memory card IRIG-B (BNC, RS 485, serial fiber) Serial and TCP/IP ports can be further expanded with the complementary eXP-9430 or eXP-9435 Comm Expansion Module	4-RS-232/RS-485 (switchable) 2-USB host ports, 1-USB Maint. Port. 2-100 BaseFX Ethernet 4-10/100 BaseTX Ethernet (RJ-45) 16-RS-232/RS-485 (switchable) or 8-Serial fiber ports (multimode w/Connectors) 16-RS-232/RS-485 (with rear panel) or DB-9 Connectors)
Hardwired I/O	Status: Maximum of 16,384 points at 64 points per I/O Interface Board Analog: Maximum of 8,192 at 32 points per I/O Interface Board Control: Maximum of 32,768 at 16 points per I/O Interface Board (up to 8 AGC raise/lower points)	
Additional I/O	Secure and configurable HTML display server (local or remote), IED engineering access	
Architecture	LINUX Real Time O/S. Option for redundant processors and/or communication with auto fail-over	
Configuration	Through secure ConfigWiz® management program and browser based test panel via local USB maintenance port or local/remote IP connection. Full complement of data filtering, port switching, IED I/O control, and other management/diagnostic functions.	
Network and Time	SNMP, NTP, PTP (IEC 1588), SNTP, IRIG-B. Supercap backup of internal real-time clock	
Programmable Logic	Programmable Logic and Communications Controller (PLCC)	
Server Protocols	DNP3.0 (serial and over IP with secure authentication), Secure FTP, L&G 8979 (Serial and over IP), Modbus (RTU and TCP), IEC-60870, IEC-61850, QUICS (Serial and over IP), Tejas/Valmet Series III/IV, BETAC, CDC I & II, Conitel 300, TRW, REDAC70H, WTP(Web Test Panel) and others	
Client Protocols	DNP3.0 (serial and over IP with secure authentication) Modbus (RTU and TCP), IEC-60870, IEC 61850, SEL Fastmeter/ASCII with device discovery, L&G 8979, PLCC, Large library of legacy SCADA protocols to interface older RTUs to upgraded, open systems and modern communication channels.	
Power	48-125 Vdc, 120/240/277 Vac (50/60 Hz, +/- 10%)	
LED Indicators	Indicators to display CPU health, power, communications, port activity and IRIG-B.	
Environment	-40° to +85°C, (-40° to 185°F) 0-95% humidity (non-condensing)	

QEI, LLC

45 Fadem Road
Springfield, NJ 07081 USA
T: +973-379-7400 F: +973-379-2138
E: sales@qeinc.com
W: www.qeinc.com

This literature is for illustration purposes only and is not part of any contract. Features may be modified at any time without notice. All trademarks and names mentioned in this document remain the exclusive property of their holder.

V 1.7 9/25

